



**JAIDEV EDUCATION SOCIETY'S**  
**J D COLLEGE OF ENGINEERING AND MANAGEMENT**  
**KATOL ROAD, NAGPUR**  
 Website: [www.jdcoem.ac.in](http://www.jdcoem.ac.in) E-mail: [info@jdcoem.ac.in](mailto:info@jdcoem.ac.in)  
 (An Autonomous Institute, with NAAC "A" Grade)  
 Affiliated to DBATU, RTMNU & MSBTE Mumbai



**Examination Section**

**VISION**

**MISSION**

To win the trust of all stakeholders in conducting the assessment and evaluation.

1. To frame and adopt procedure for various works involved in accountability.
2. To utilize the resources as per expertise of individual and maintaining good quality and standard of assessment work.
3. To ensure that the student participate in assessment process enthusiastically.

**OFFICE OF CONTROLLER OF EXAMINATIONS**

**Time Table of M.Tech I - Sem Winter 2025 (Regular) – Theory Examination**

Time: 09.30 AM to 12.00 PM

Date: 19-11-2025

Branch / Day & Date of Exam	Friday	Monday	Wednesday	Friday	Monday
	26-12-2025	29-12-2025	31-12-2025	02-01-2026	05-01-2026
Computer Science & Engineering	Computer Algorithms	Advanced Operating System	Program Elective-I	High Performance Computing	Open Elective-I
Electronics Engineering	High Speed Semiconductor Devices & Circuits	VLSI System Design	Professional Elective-I	Computational Methods	Open Elective -I
Structural Engineering (CE)	Matrix Analysis of Structures	Structural Dynamics	Program Elective-I	New Engineering Materials	Open Elective-I
Electrical Power Systems (EE)	Advance Power Electronics	Power System Analysis	Professional Elective-I	Computer Programming in Python	Open Elective-I

**Practical Examination**

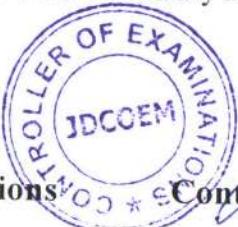
Time: 09.30 AM to 04.30 PM

Branch / Day & Date of Exam	Monday	Tuesday	Wednesday
	22-12-2025	23-12-2025	24-12-2025
Computer Science & Engineering	Computer Algorithms	Advanced Operating Systems	Graphics Designing
Electronics Engineering	PG Lab	VLSI System Design	Graphic Design
Structural Engineering (CE)	Matrix Analysis of Structures Lab	Structural Dynamics Lab	Computer aided analysis and design by using Staad Pro
Electrical Power Systems (EE)	PG Lab	Advance Power Electronics Lab	Power System Simulation Lab

**Copy to:**

1. Hon'ble Directors, JES
2. All Dean's / H.O.D.'s / SH's for necessary action.

Dy. Controller of Examinations



Controller of Examinations

*G. J. Gohel*

*D. Patel*  
Principal